

FIG. 1
(PRIOR ART)

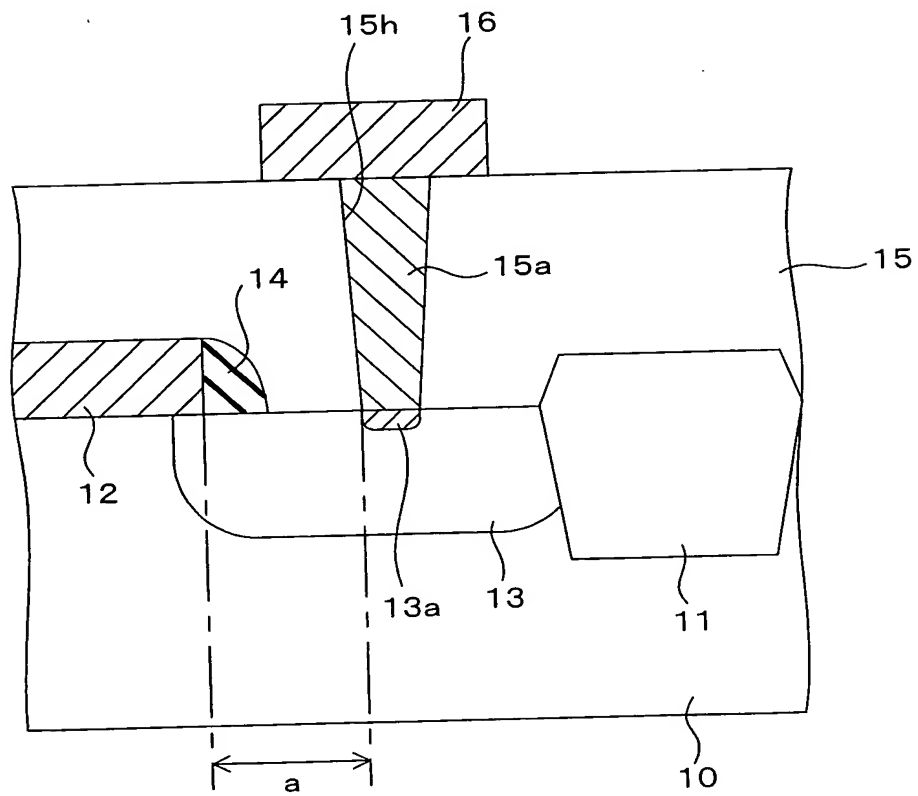


FIG. 2
(PRIOR ART)

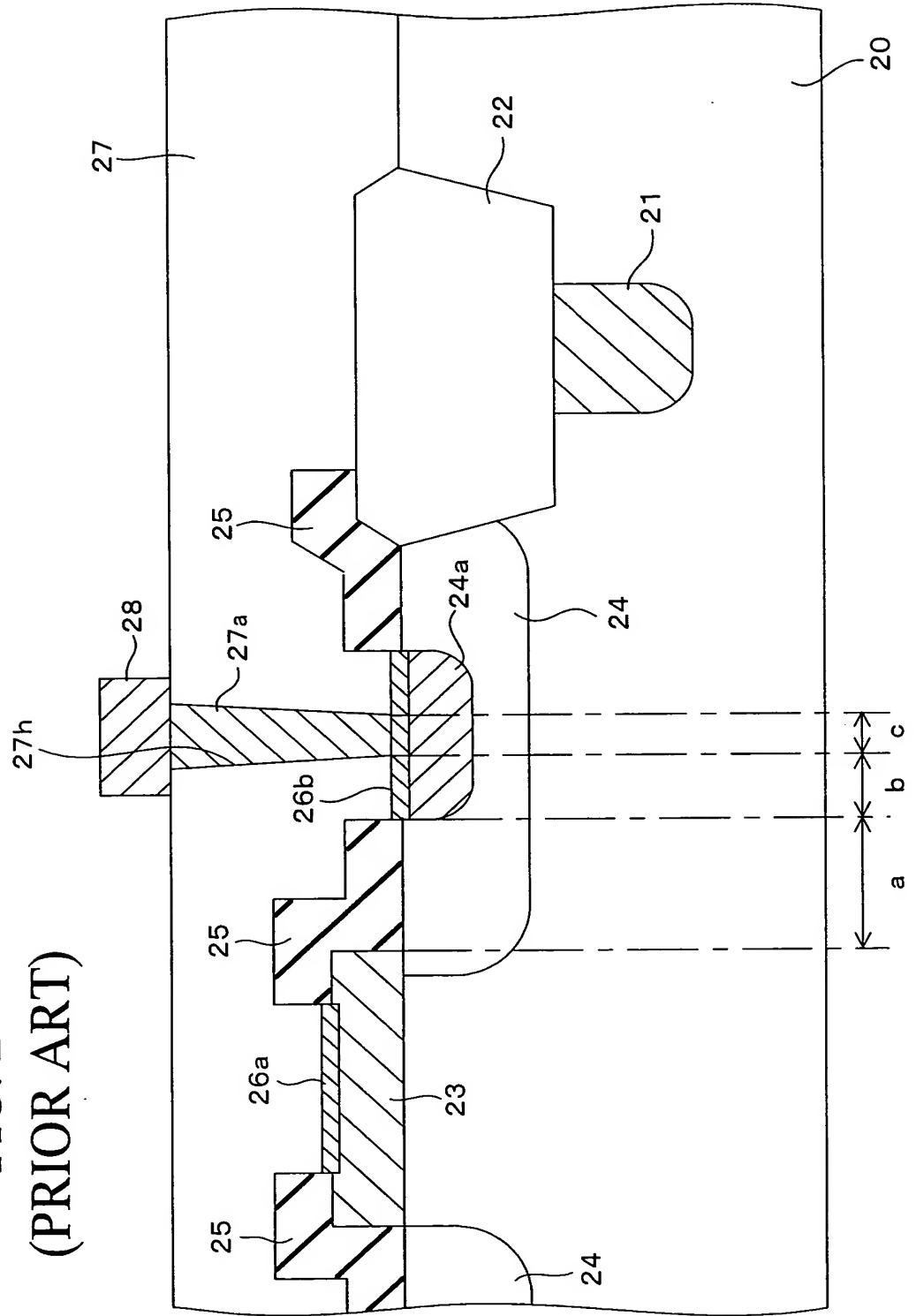


FIG. 3A

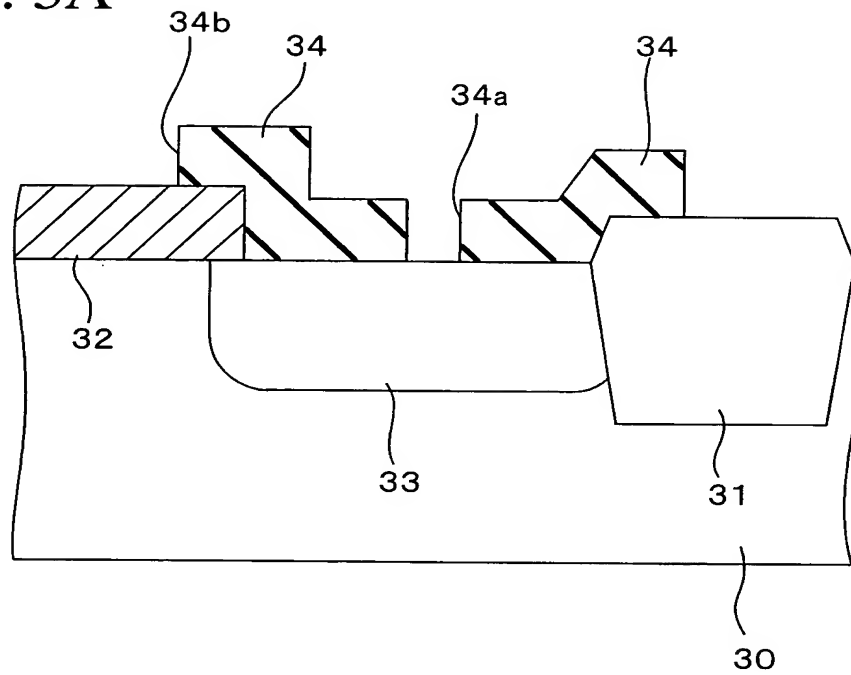


FIG. 3B

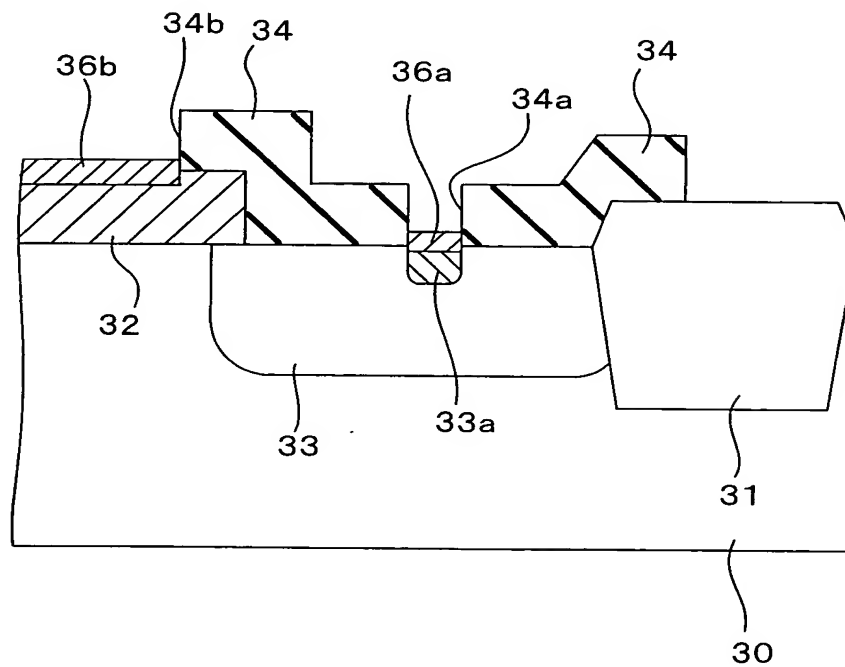


FIG. 3C

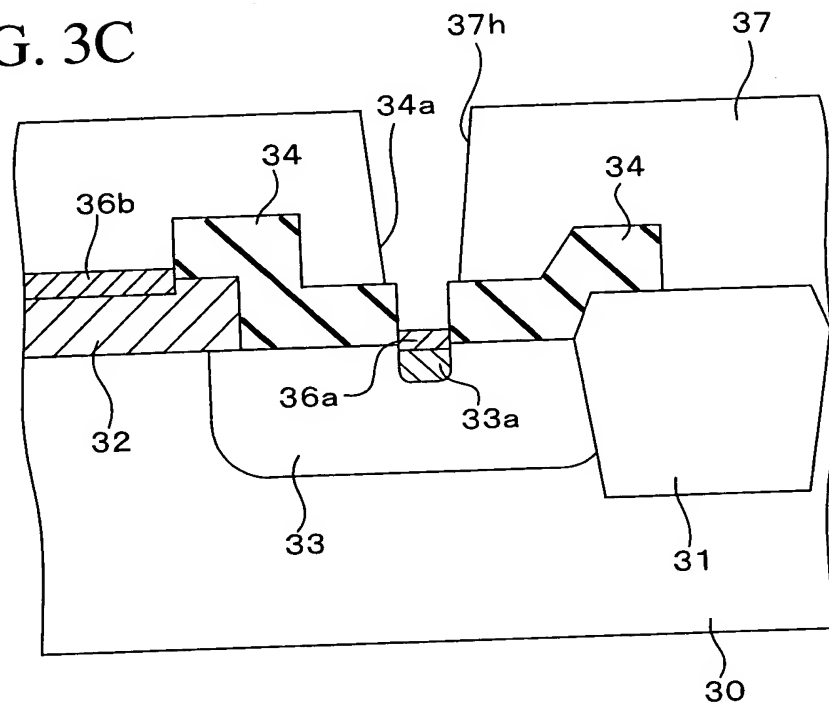


FIG. 3D

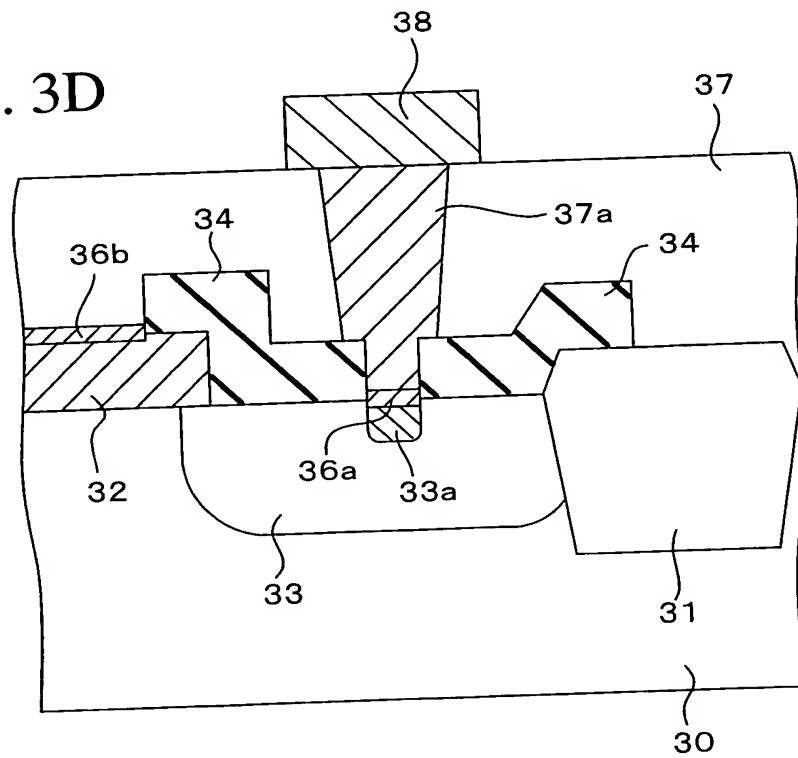


FIG. 4

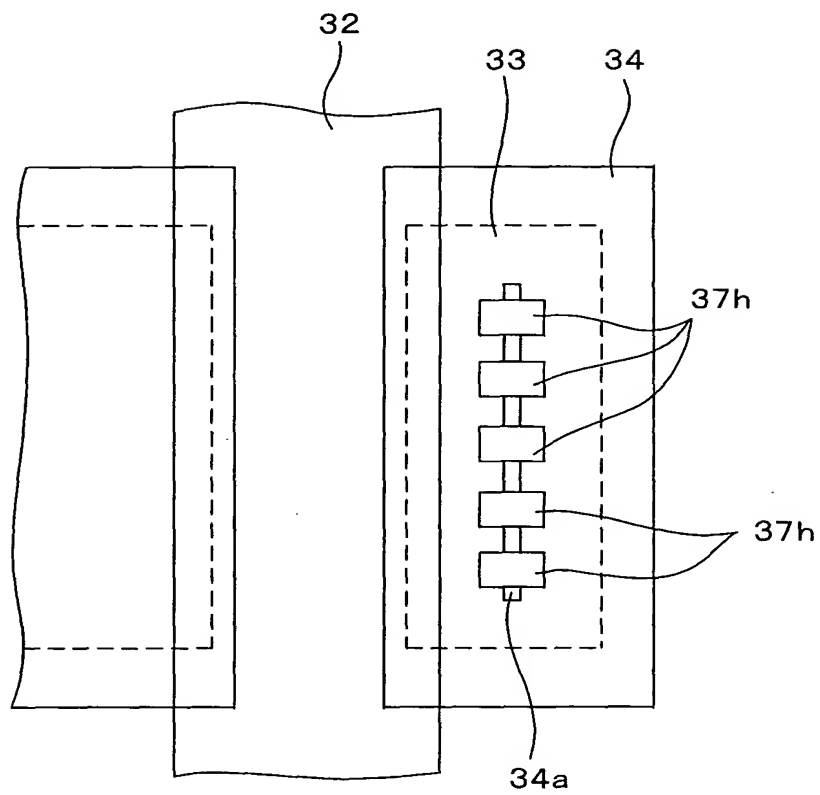


FIG. 5A

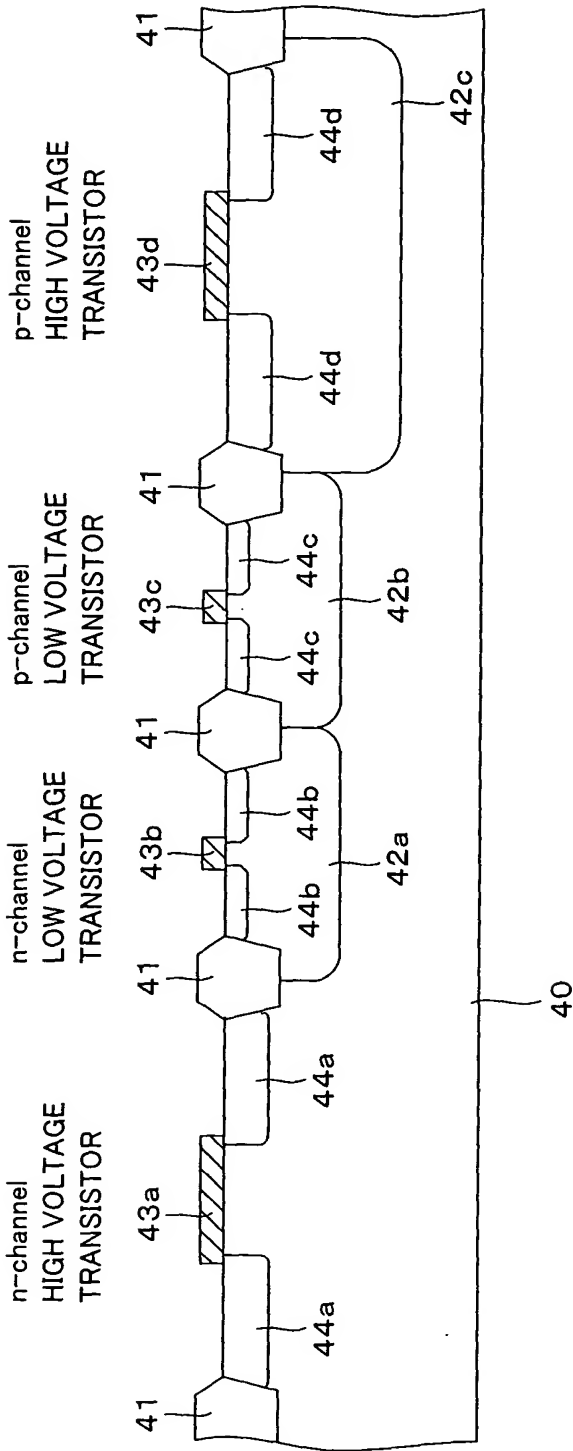


FIG. 5B

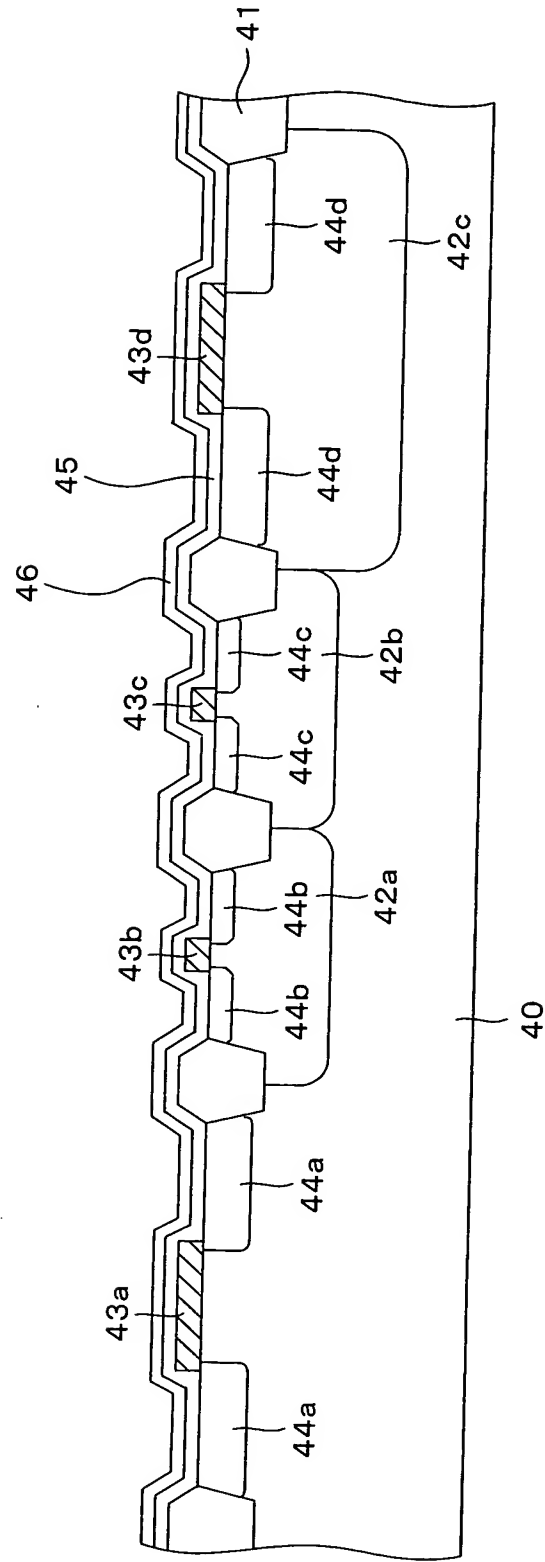


FIG. 5C

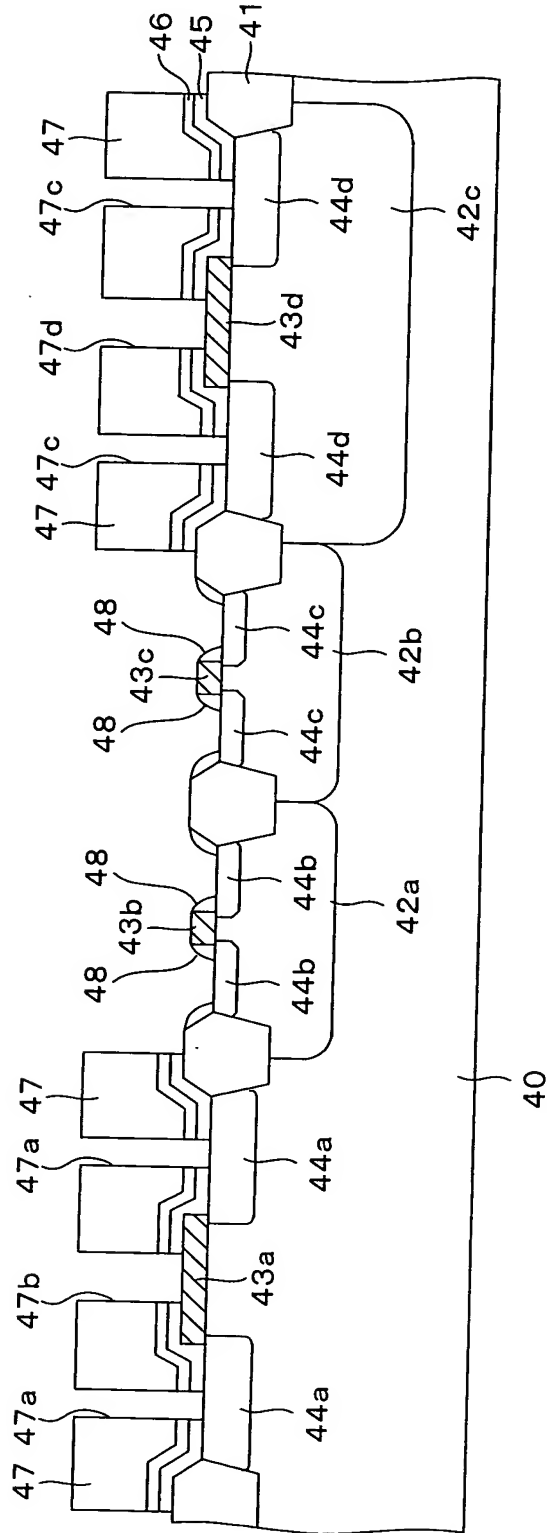


FIG. 5D

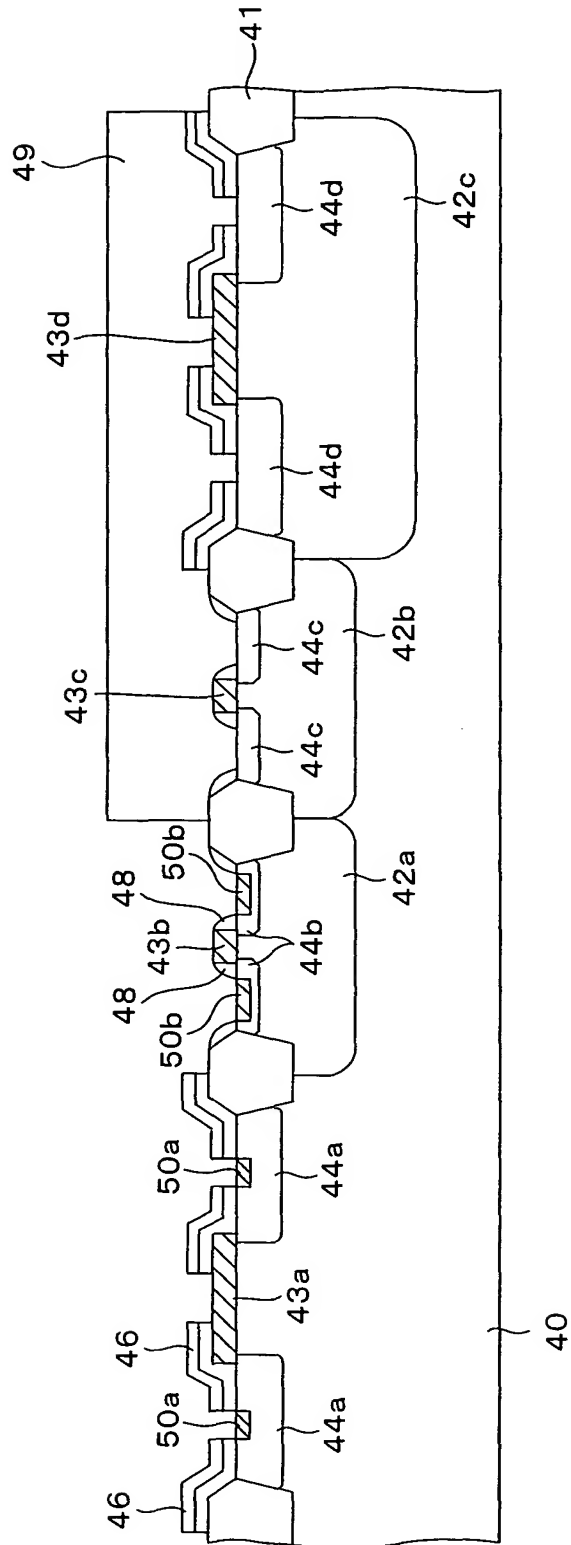


FIG. 5E

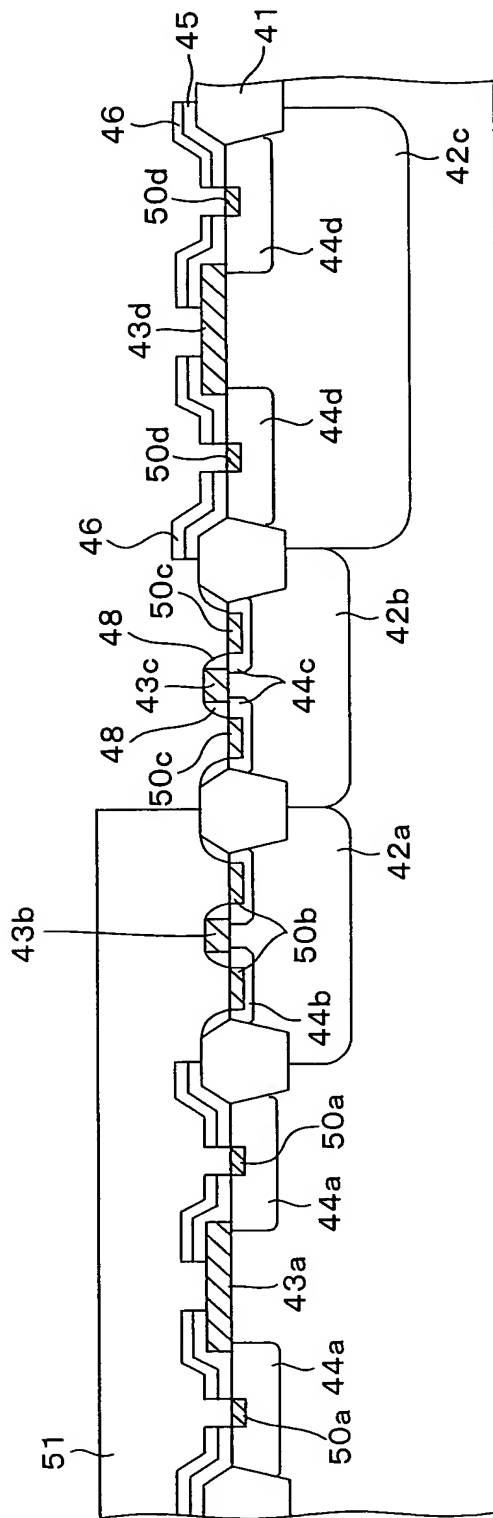


FIG. 5F

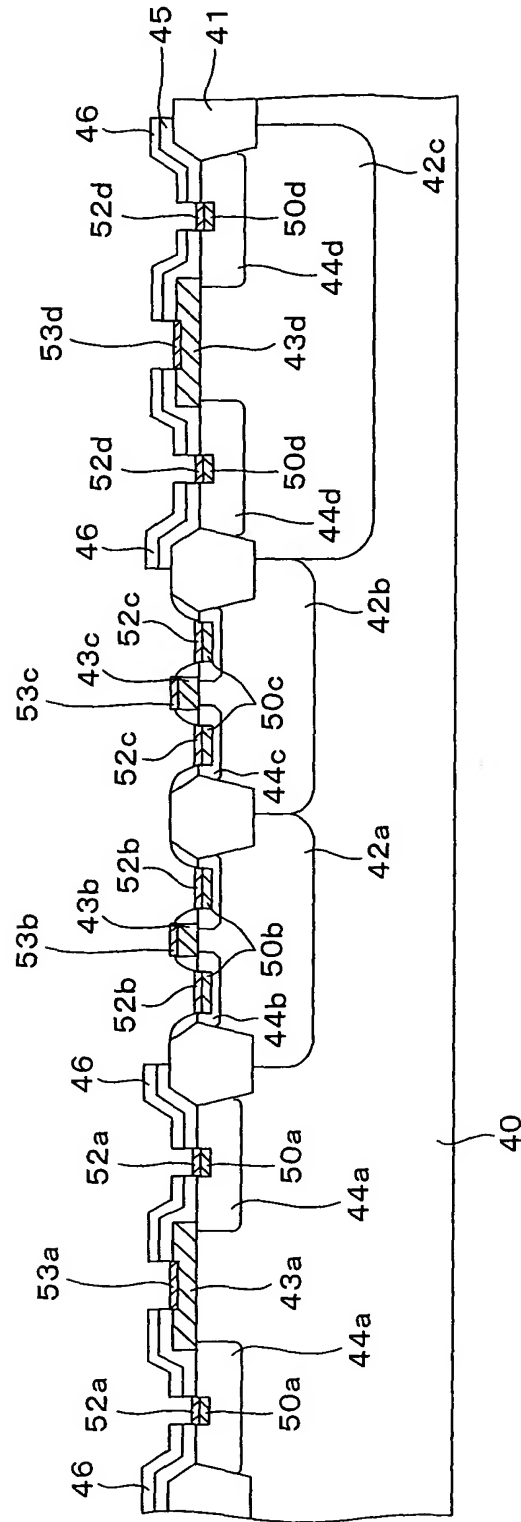


FIG. 5G

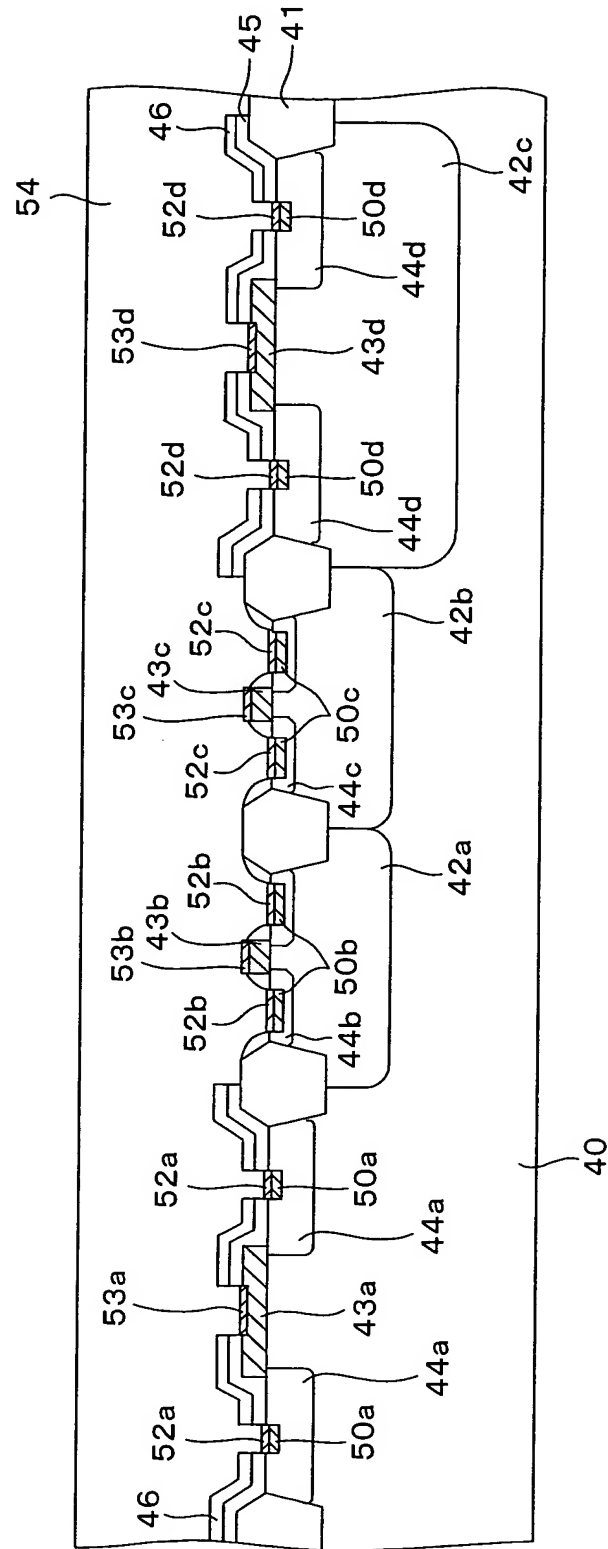


FIG. 5H

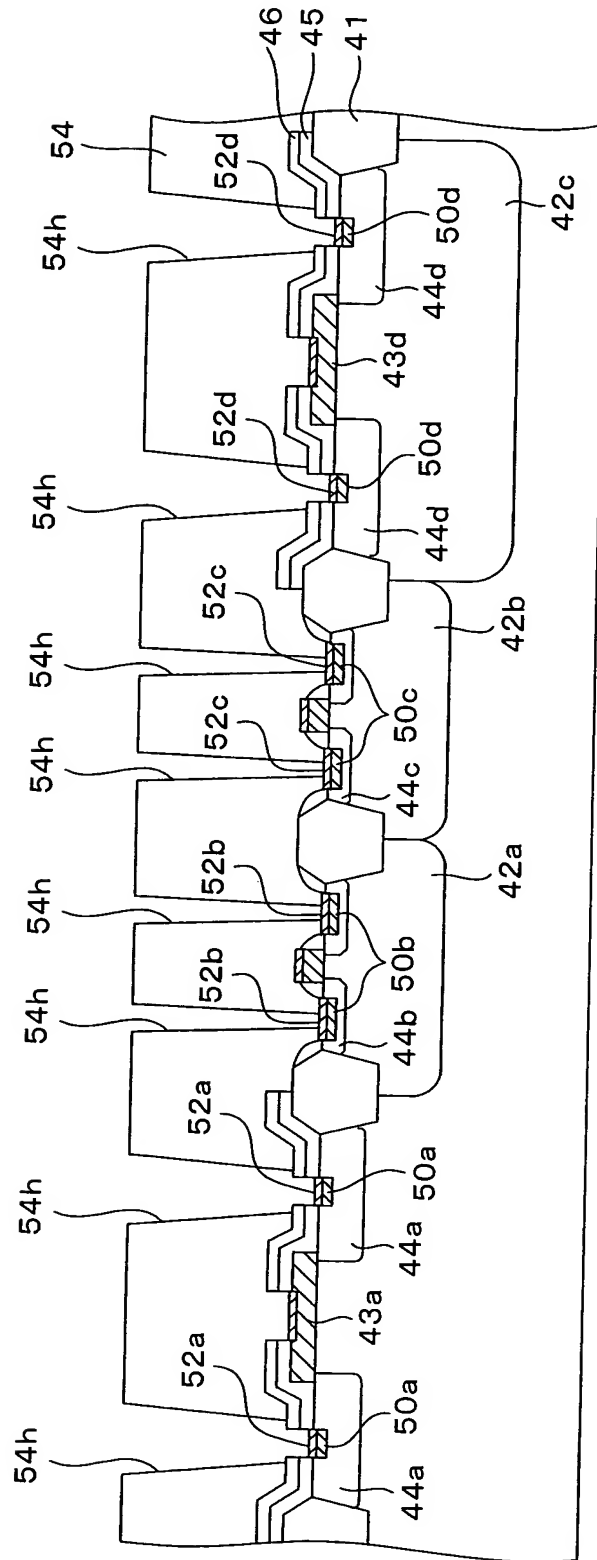


FIG. 5I

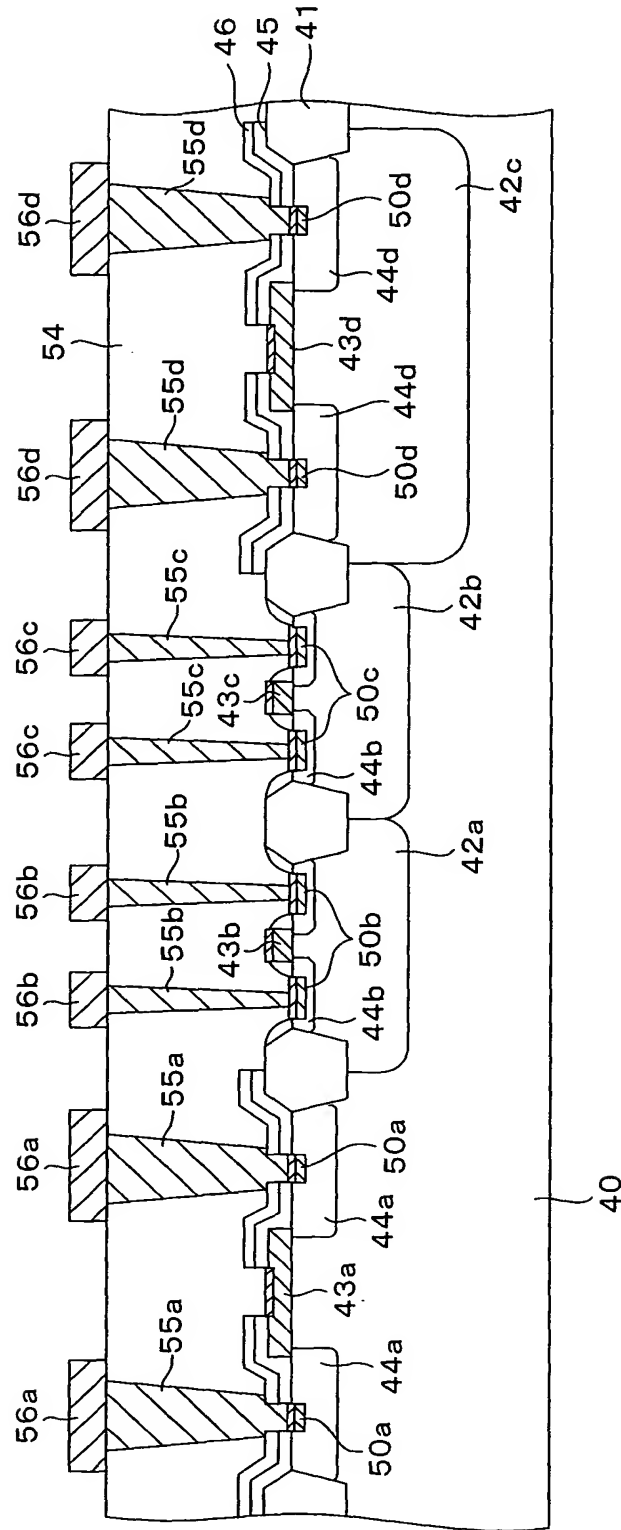


FIG. 6A

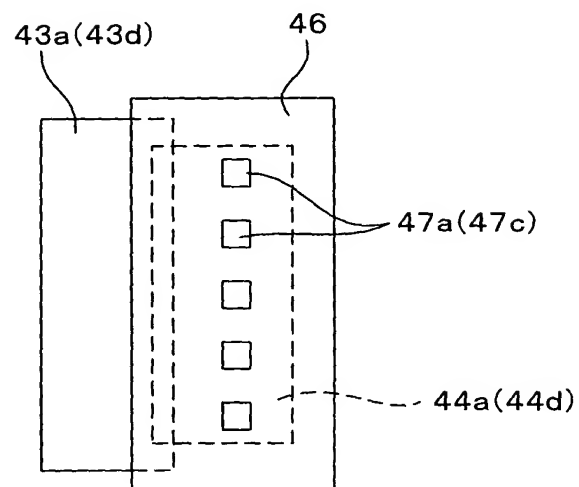


FIG. 6B

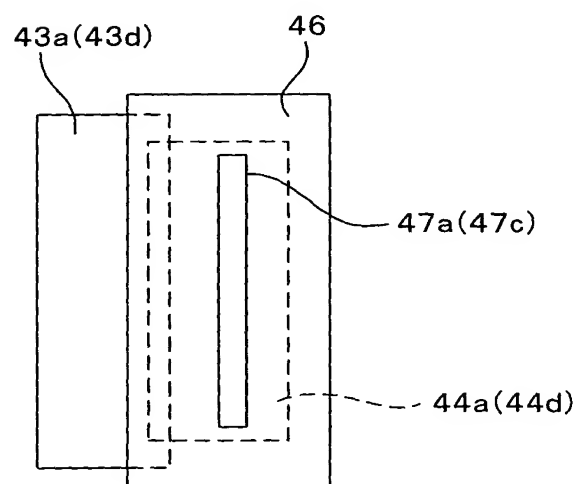


FIG. 7A

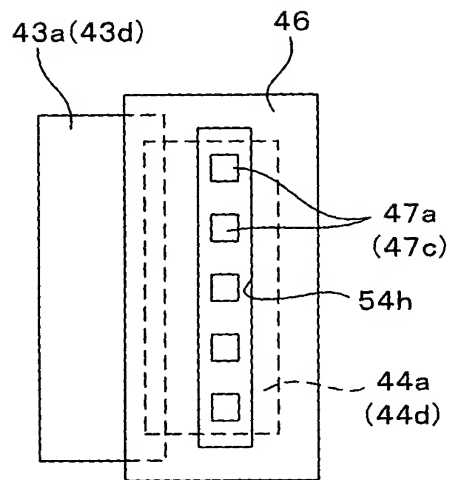


FIG. 7B

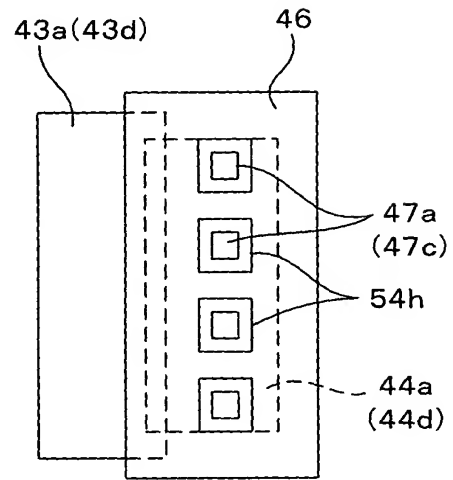


FIG. 7C

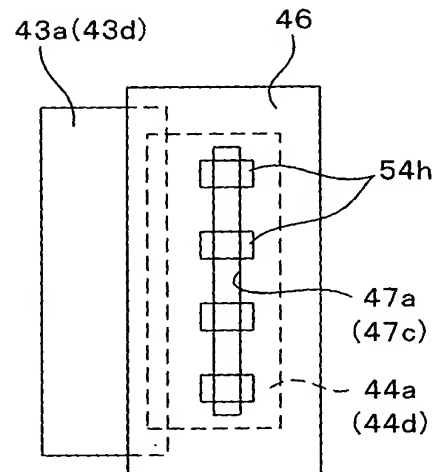


FIG. 7D

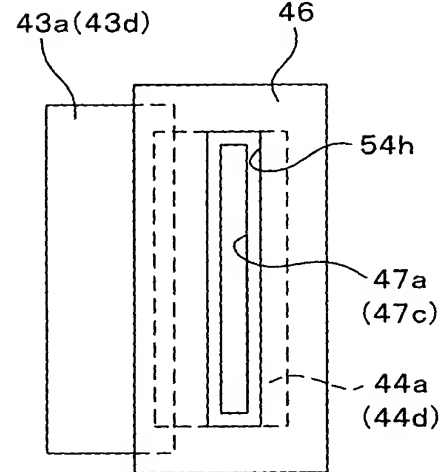


FIG. 8A

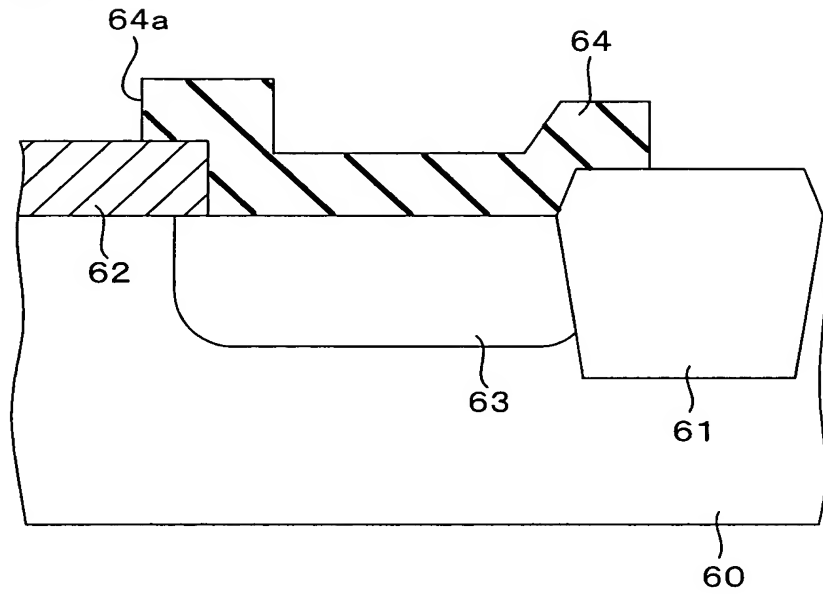


FIG. 8B

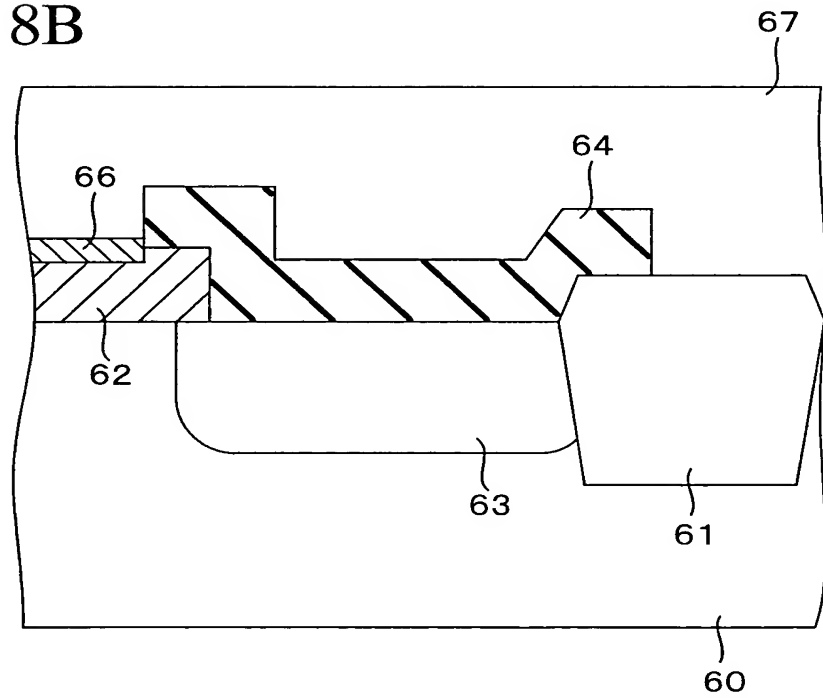


FIG. 8C

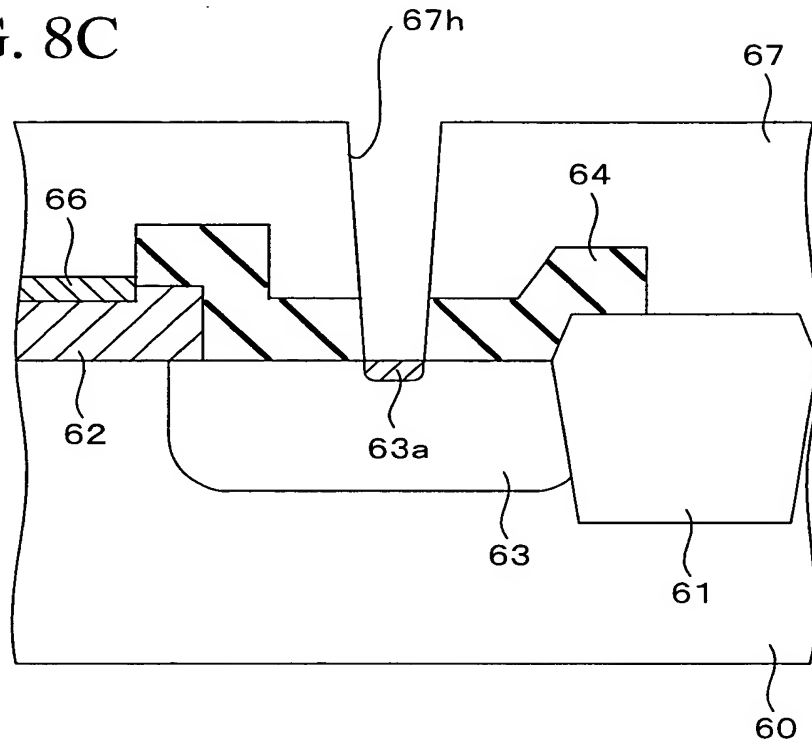


FIG. 8D

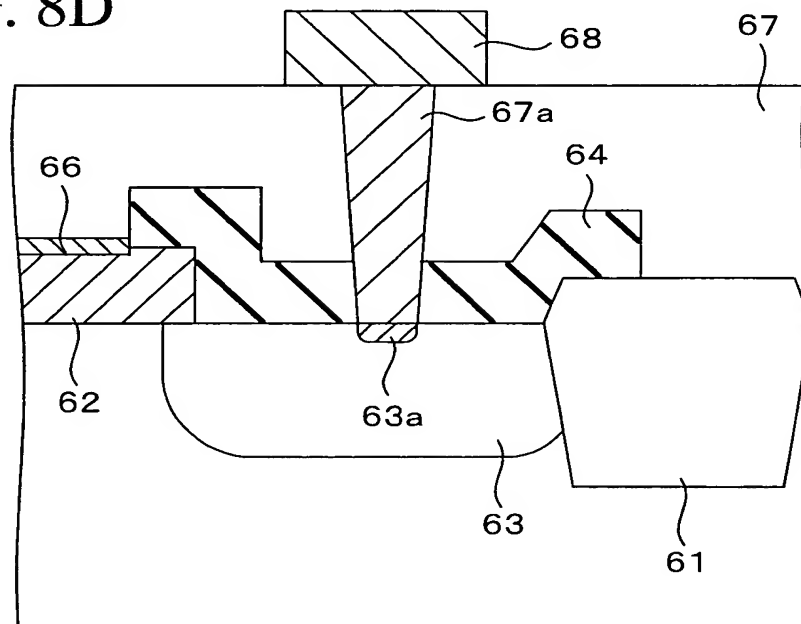


FIG. 9

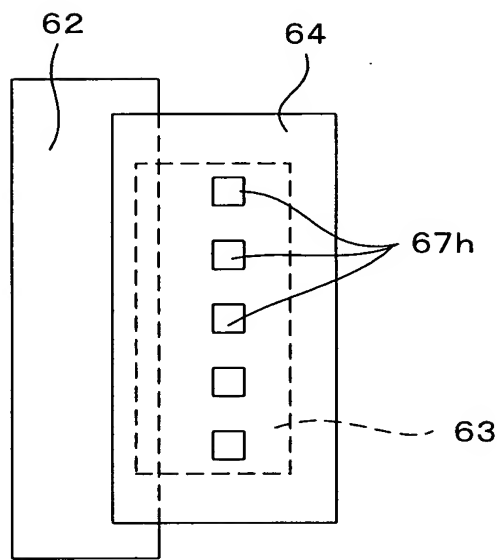


FIG. 10A

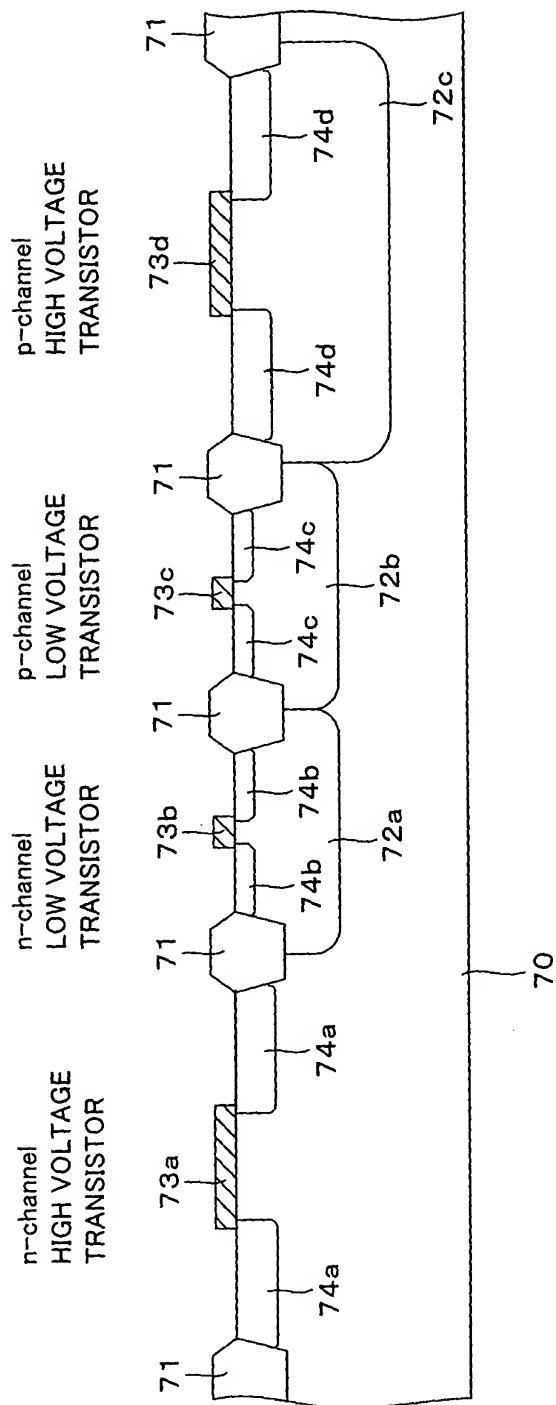


FIG. 10B

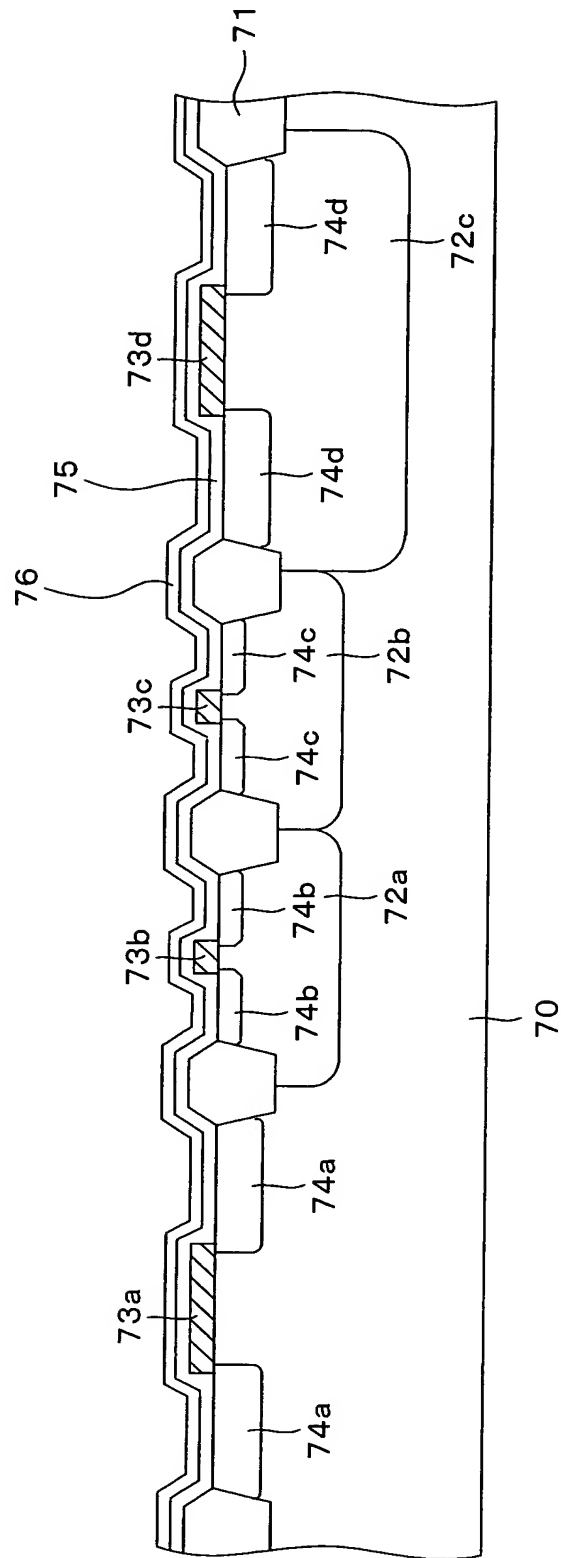
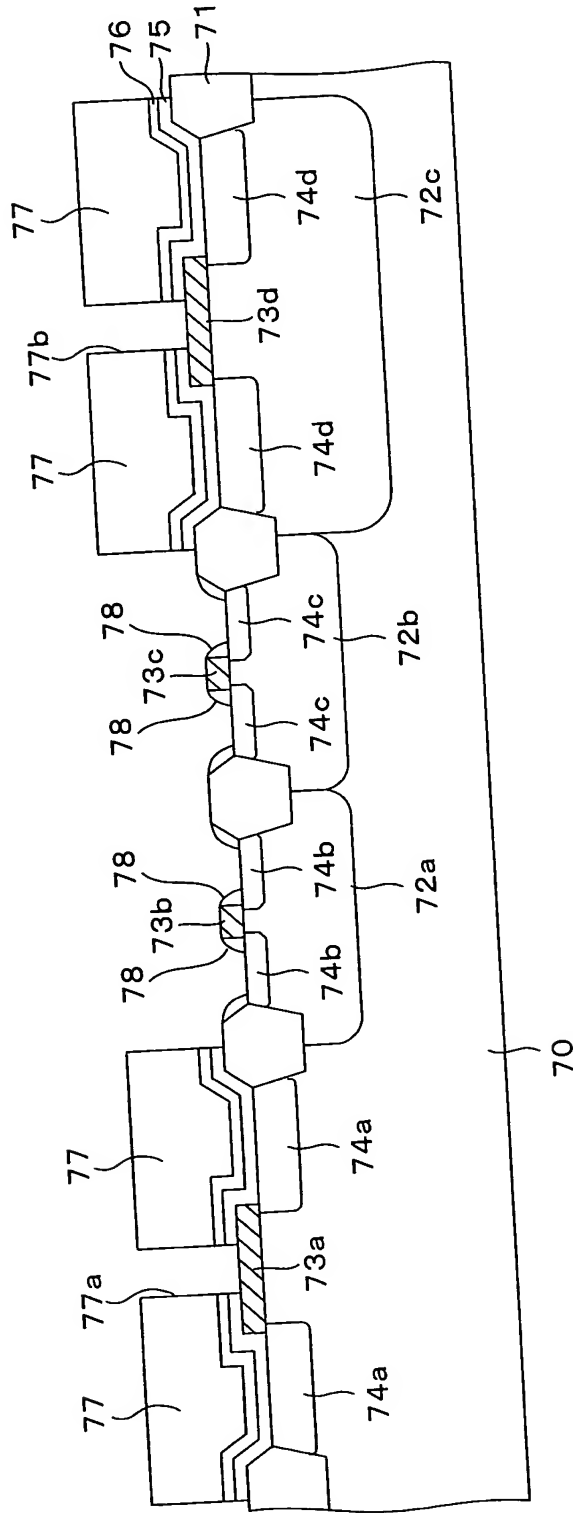


FIG. 10C



A detailed cross-sectional view of a multi-layered electronic device 70. The device consists of a top layer 71, a middle layer 72, and a bottom layer 73. The middle layer 72 is divided into three regions: 72a, 72b, and 72c. Region 72a contains a central core 74a and two side cores 74b. Region 72b contains a central core 74c and two side cores 74d. Region 72c contains a central core 74e and two side cores 74f. The top layer 71 has a central core 75 and two side cores 76. The bottom layer 73 has a central core 77 and two side cores 78. The device is further divided into three main sections: 73a, 73b, and 73c. Section 73a contains the top layer 71, middle layer 72a, and bottom layer 73a. Section 73b contains the top layer 71, middle layer 72b, and bottom layer 73b. Section 73c contains the top layer 71, middle layer 72c, and bottom layer 73c. The device is shown in a perspective view, with the top layer 71 and bottom layer 73 being rectangular and the middle layer 72 being a central strip.

FIG. 10E

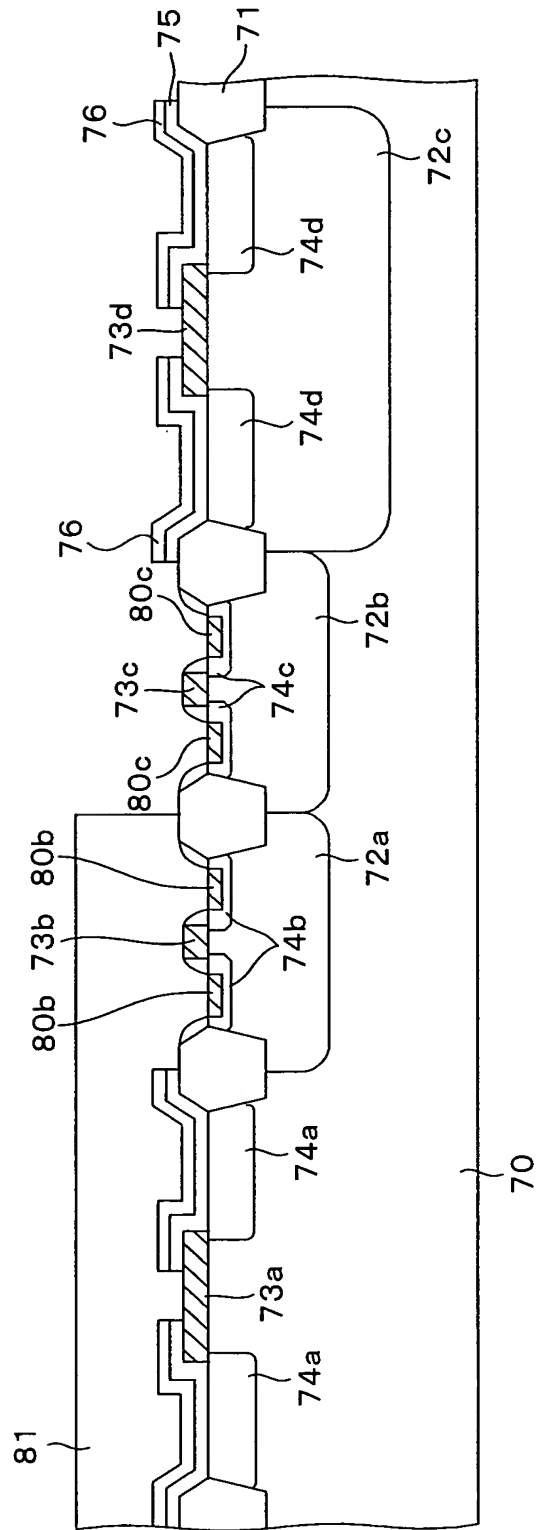


FIG. 10F

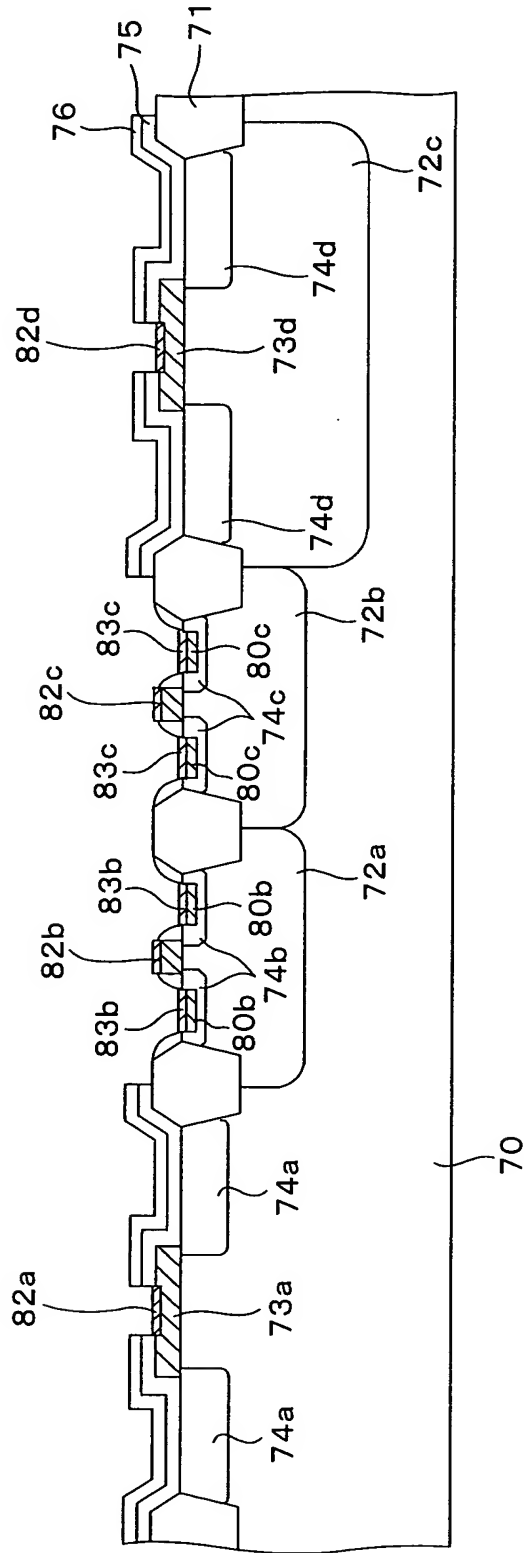


FIG. 10G

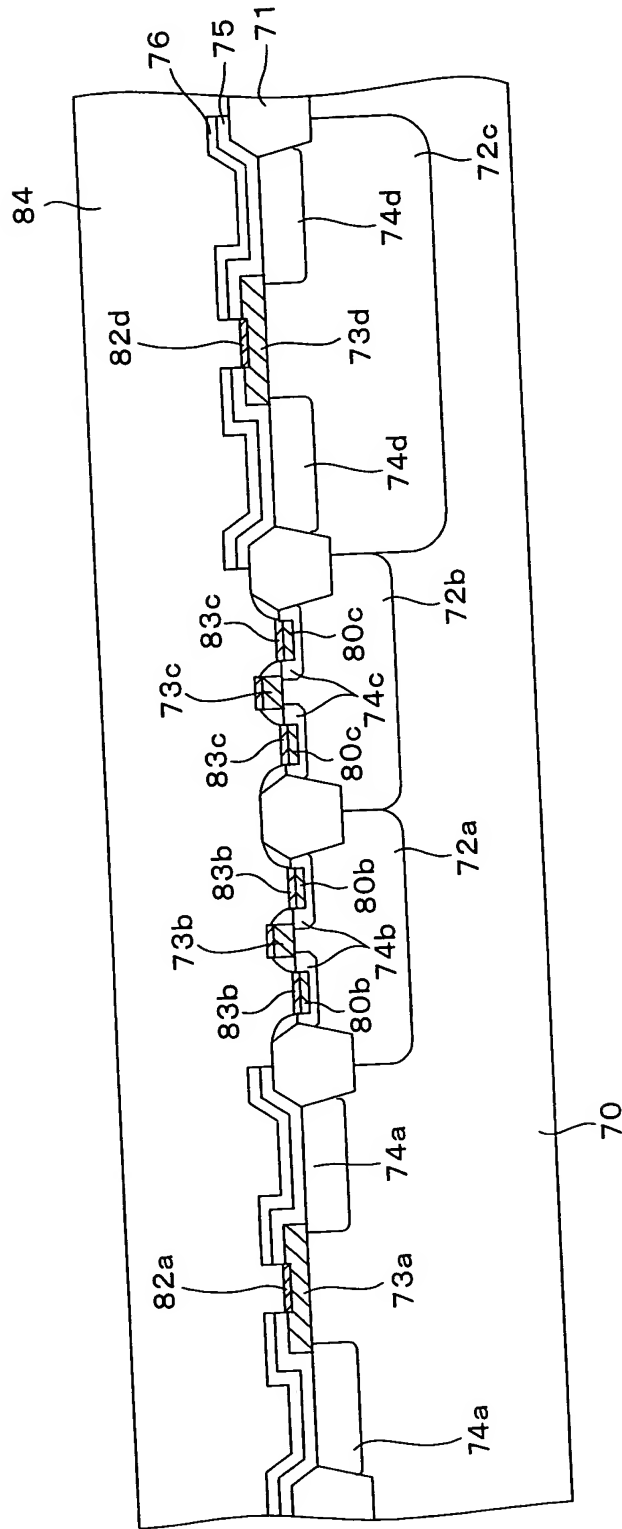


FIG. 10H

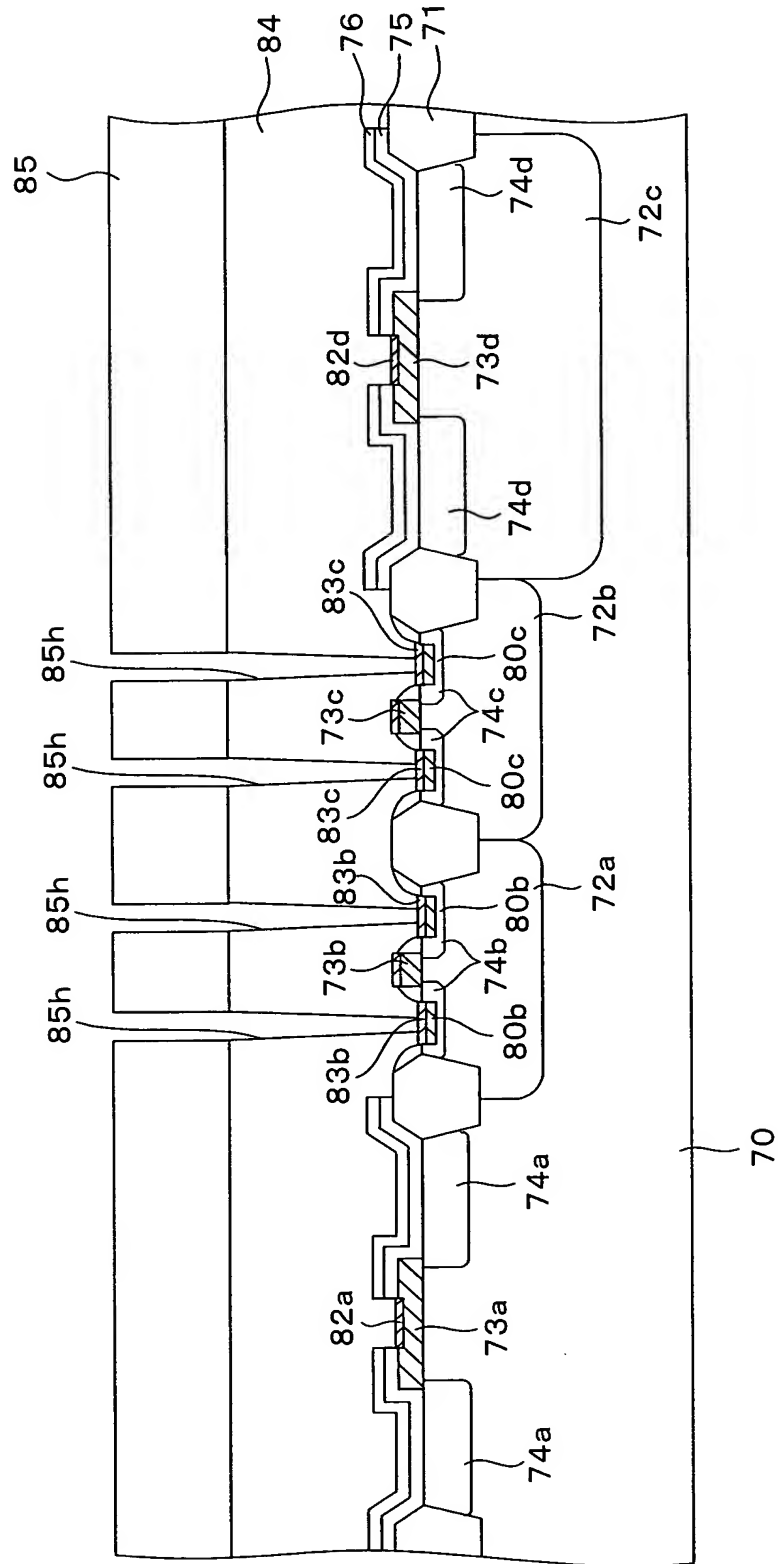


FIG. 10I

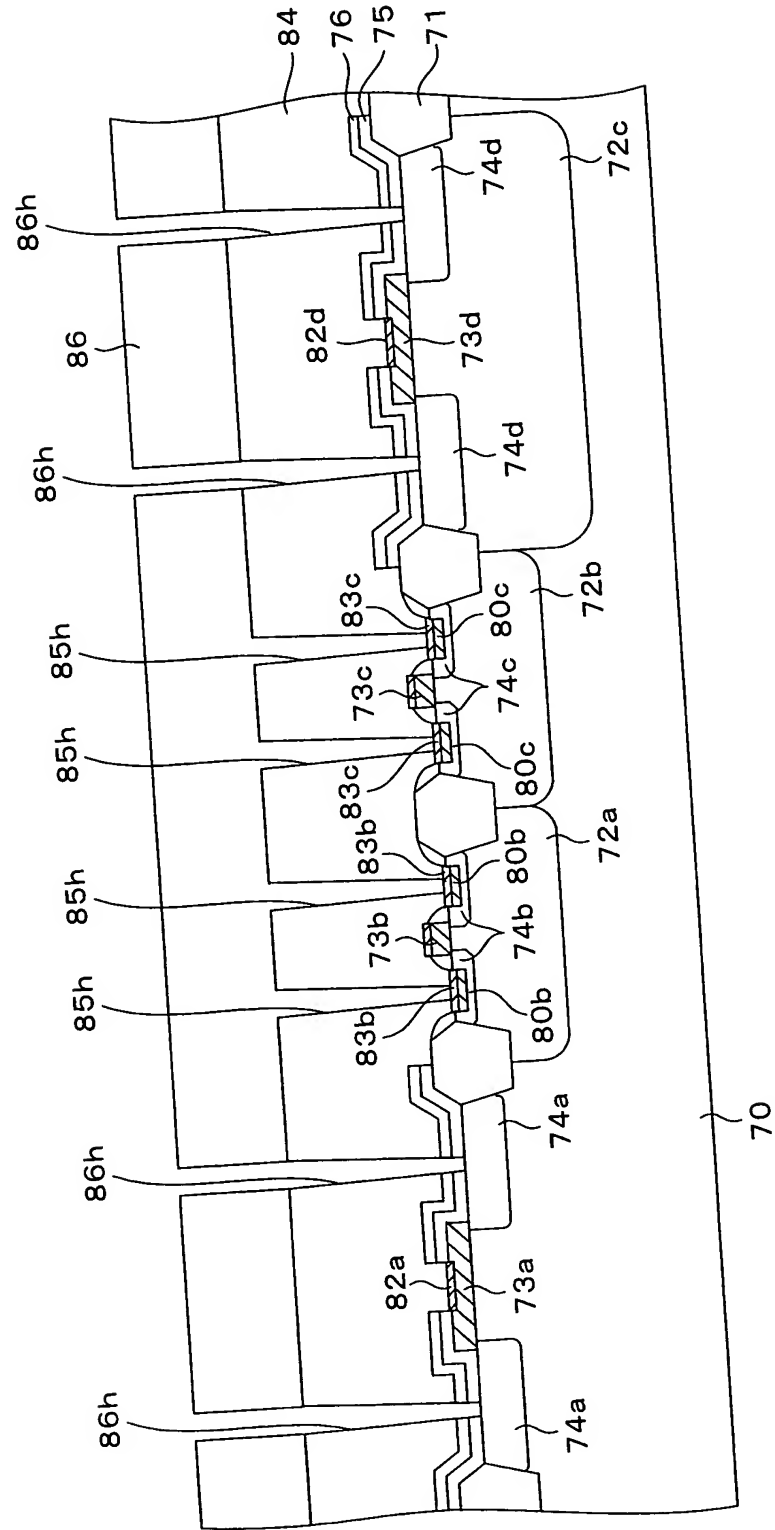


FIG. 10J

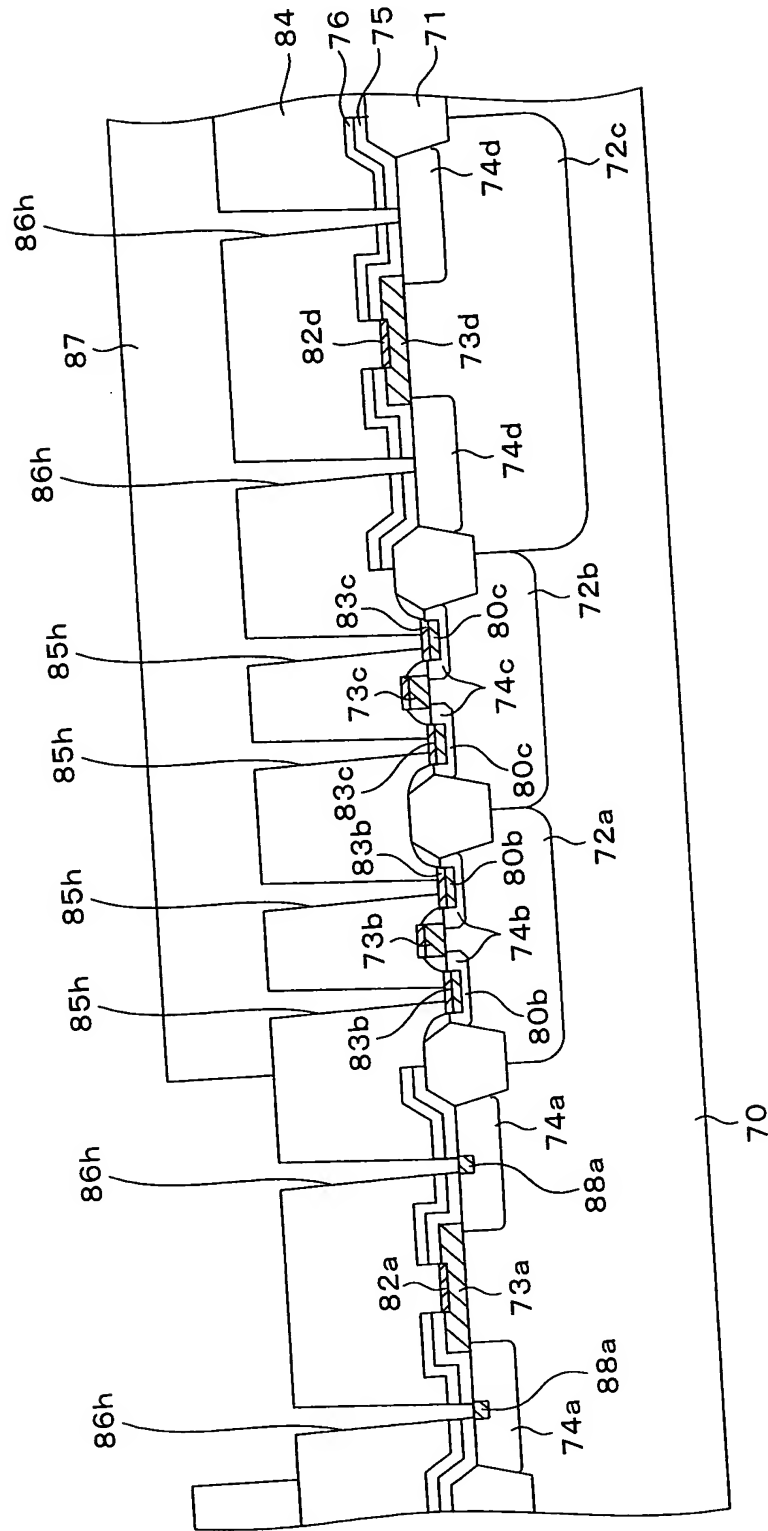
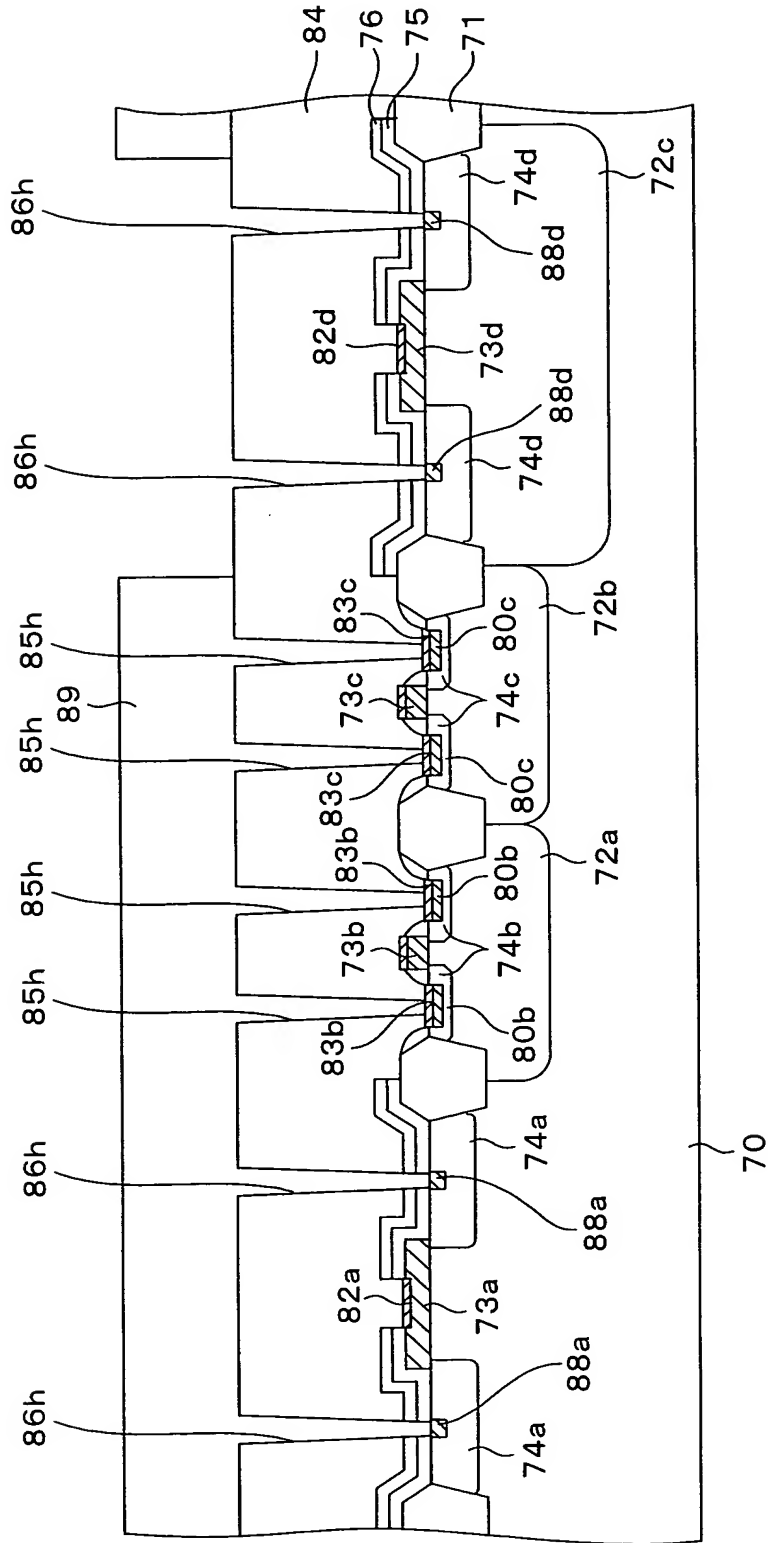


FIG. 10K



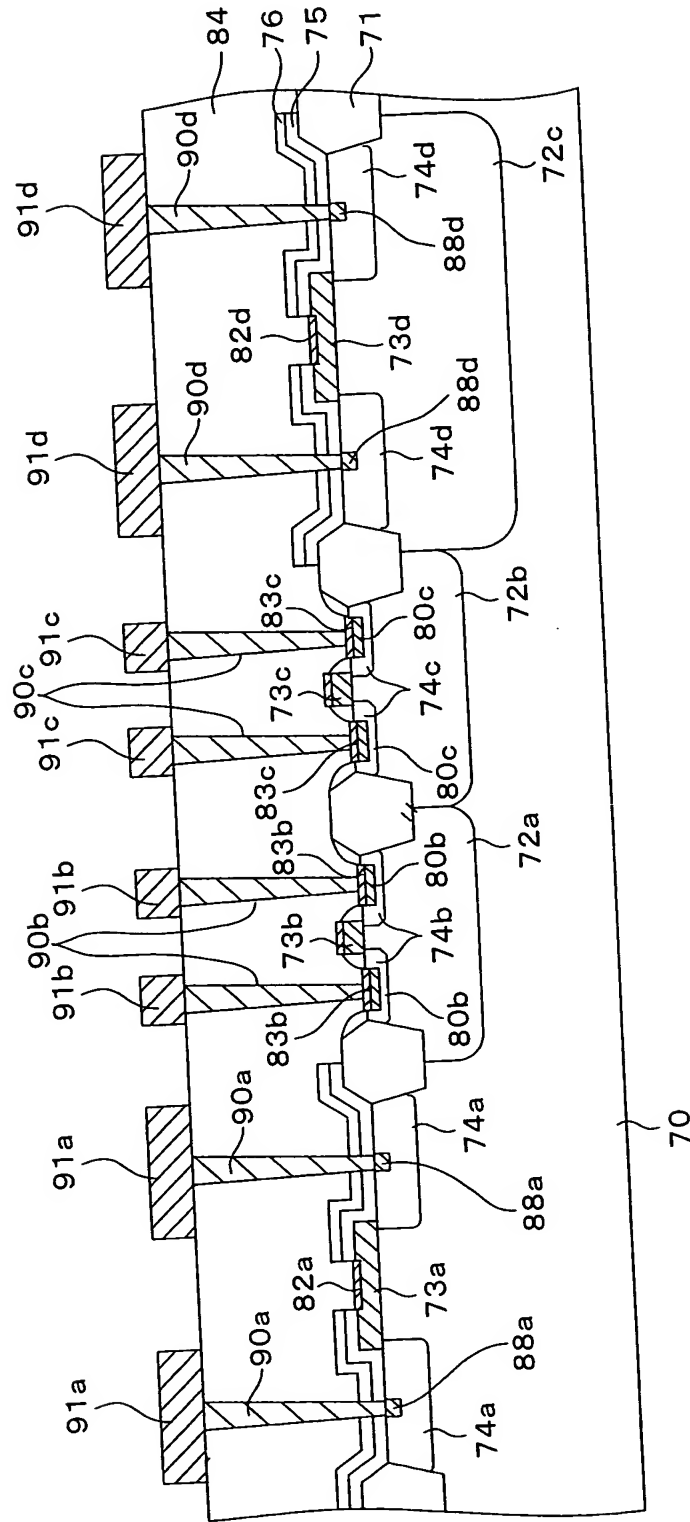


FIG. 11A

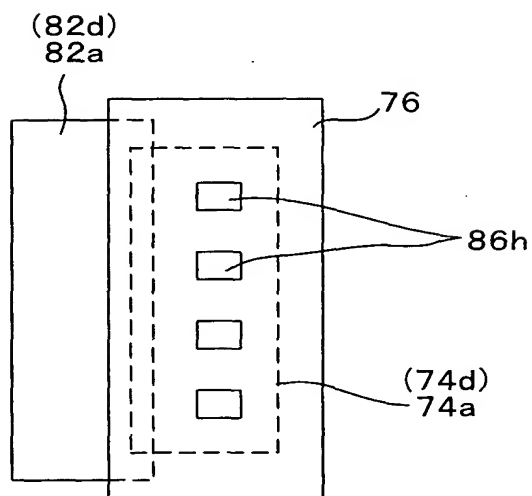


FIG. 11B

